# 2.1 Algorithms – L2 Activity 1

## Diagram – Internal Components of a Computer System

## High Ability – Label the following Diagram

0001

0010

0011

0100

0101

0110

0111

1000

1001

1010

1011

1100

1101

INP

Add 5

INP

STO 8

INP

ADD 8

1. Find definitions of the following

|  |  |
| --- | --- |
| **Processor** |  |
| **MAR** |  |
| **MDR** |  |
| **CIR** |  |
| **Main Memory** |  |
| **Address Bus** |  |
| **Data Bus** |  |
| **Accumulator** |  |
| **PC (Program Counter)** |  |
| **ALU** |  |

1. Now label where you believe they should be in the diagram.
2. What is meant by the Fetch-Execute Cycle?
3. Describe in words the stages of the fetch-execute cycle using the diagram that has just been completed.
4. Watch the video located: <https://www.youtube.com/watch?v=xfJbpCJSpd8> to check your answers.